



**Interactive Display Solutions, INC.**

490 Wald, Irvine, CA 92618

TEL: +1 (949) 727 1959 / FAX: +1 (949) 727 9494

## PRODUCT SPECIFICATION

<b>IDS Part Number</b>	<b>1057-A04003-00</b>
<b>Version</b>	
<b>Customer</b>	
<b>Approved By</b>	
<b>Date</b>	

- Approved For Specifications**
- Approved For Specifications & Sample**

**Prepared By:**

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**Confirmed By:**

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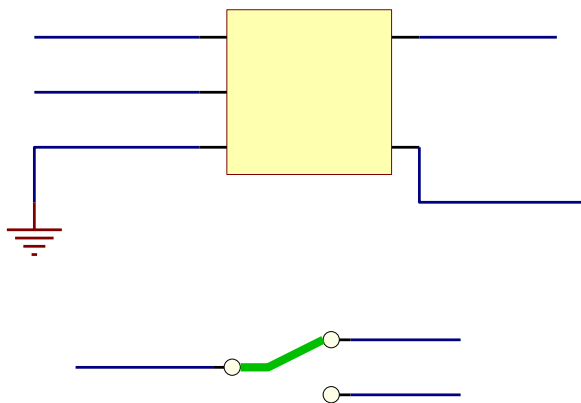
## RECORD OF REVISION

Revision Date	Page	Contents	Editor
2015/12/16	-	New Release	Kokai

# 1 Features

5.7 inch Amorphous-TFT-LCD (Thin Film Transistor Liquid Crystal Display) module. This module is composed of a 5.7" TFT-LCD panel, a driver circuit and backlight unit.

- (1) Construction: 5.7" a-Si color TFT-LCD, White LED Backlight and PCB.
- (2) Resolution (pixel): 320(R.G.B) X240
- (3) Number of the Colors : 262K colors ( R , G , B 6 bit digital each)
- (4) LCD type : Transmissive Color TFT LCD ( normally White)
- (5) Interface: 33 pin
- (6) Power Supply Voltage: 3.3V single power input. Built-in power supply circuit.
- (7) Viewing Direction: 12 O'clock ( Gray Inversion )
- (8) PCB REV.D ; DCLK improved. RV10=9.1K



## 2 Physical specifications

Item	Specifications	Unit
Display resolution(dot)	320 (W) x RGB x 240(H)	mm
Active area	115.2 (W) x 86.4 (H)	mm
Screen size	5.7(Diagonal)	mm
Pixel size	120 (W) x 360 (H)	um
Color configuration	R.G.B stripe	
Overall dimension	144.0(W)x104.6(H)x13.0(D)	mm
Weight	T.B.D	mg
Backlight unit	LED	

## 3 Electrical specification

### 3.1 Absolute max. ratings

#### 3.1.1 Electrical Absolute max. ratings

Item	Symbol	Condition	Min.	Max.	Unit	Remark
Power voltage	VCC	VSS=0	-0.3	6.0	V	
Input voltege	V <sub>in</sub>		-0.3	VCC+0.3	V	Note 1

Note1:Hsync, Vsync, ENAB, CK, R0~R5, G0~G5, B0~B5

### 3.1.2 Environmental Absolute max. ratings

Item	OPERATING		STORAGE		Remark
	MIN	MAX	MIN	MAX	
Temperature	-20	70	-30	80	Note2,3,4,5,7
Humidity	Note1		Note1		
Corrosive Gas	Not Acceptable		Not Acceptable		

Note1 :  $T_a \leq 40^\circ\text{C}$  : 85% RH max

$T_a > 40^\circ\text{C}$  : Absolute humidity must be lower than the humidity of 85%RH at  $40^\circ\text{C}$

Note2 : For storage condition  $T_a$  at  $-30^\circ\text{C} < 48\text{h}$  , at  $80^\circ\text{C} < 100\text{h}$

For operating condition  $T_a$  at  $-20^\circ\text{C} < 100\text{h}$

Note3 : Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note4 : The response time will be slower at low temperature.

Note5 : Only operation is guaranteed at operating temperature. Contrast , response time, another display quality are evaluated at  $+25^\circ\text{C}$

## 3.2 Electrical characteristics

### 3.2.1 DC Electrical characteristic of the LCD

Typical operating conditions (VSS=0V)

Item	Symbol	Min.	Typ.	Max.	Unit	Remark	
Power supply	VCC	3.0	3.3	3.6	V		
Input Voltage for logic	H Level	$V_{IH}$	0.7 VCC	-	VCC	V	Note 1
	L Level	$V_{IL}$	0	-	0.3 VCC	V	
Power Supply current	ICC		45	55	mA	Note 2	

Note1: Hsync, Vsync, DEN, DCLK, R0~R5, G0~G5, B0~B5

Note2:  $f_v = 60\text{Hz}$ ,  $T_a = 25^\circ\text{C}$ , Display pattern : All Black

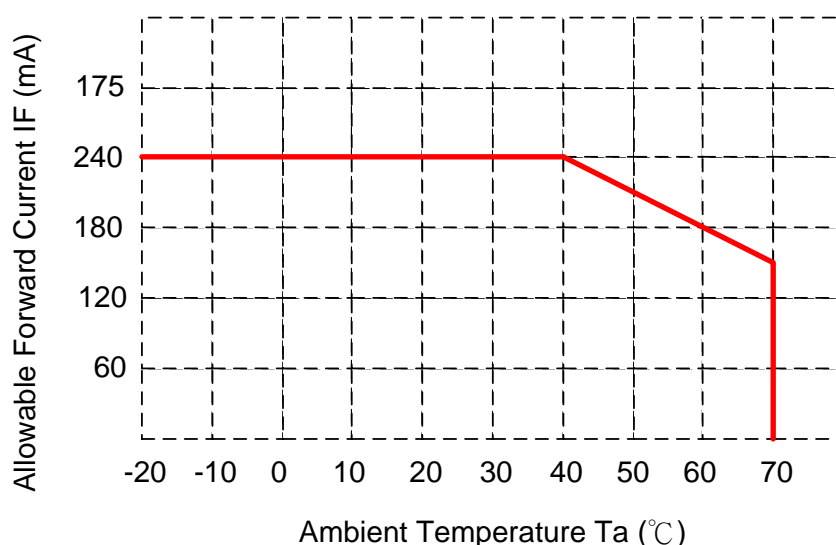
### 3.2.2 Electrical characteristic of LED Back-light

Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
LED voltage	$V_{AK}$	--	10.2	--	V	$I_{LED} = 240\text{mA}, T_a = 25^\circ\text{C}$
LED forward current	$I_{LED}$	--	240	--	mA	$T_a = 25^\circ\text{C}$
	$I_{LED}$	--	180	225	mA	$T_a = 60^\circ\text{C}$
Lamp life time		20,000	30,000	--	Hr	$I_{LED} = 240\text{mA}, T_a = 25^\circ\text{C}$

- The constant current source is needed for white LED back-light driving.

When LCM is operated over  $60^\circ\text{C}$  ambient temperature, the  $I_{LED}$  of the LED

back-light should be adjusted to 225mA max



### 3.3 AC Timing characteristic of the LCD

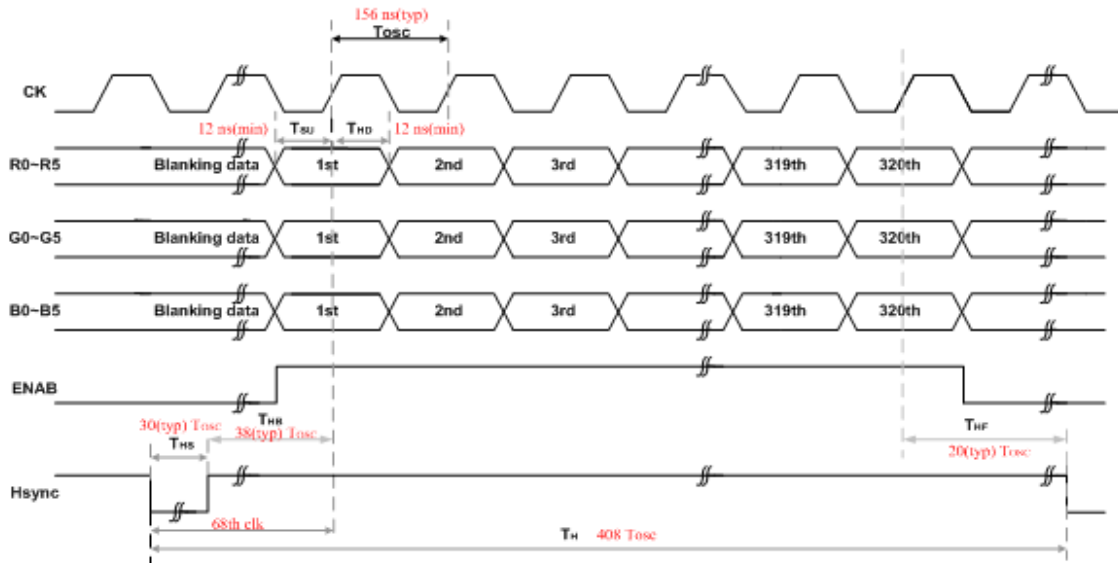
#### a. Timing condition

Signal	Parameter	Symbol	Min.	Typ.	Max	Unit.	Remark	
DCLK	DCLK period	<b>TOSC</b>	-	156	-	ns		
	Frequency	<b>FOSC</b>	-	6.4	-	MHz		
	DCLK High plus width	<b>TCH</b>	-	78	-	ns		
	DCLK Low plus width	<b>TCL</b>	-	78	-	ns		
RGB DATA	Data setup time	<b>TSU</b>	12	-	-	ns		
	Data hold time	<b>THD</b>	12	-	-	ns		
Hsync	Hsync period	<b>TH</b>	-	408	-	<b>TOSC</b>		
	Hsync pulse width	<b>THS</b>	5	30	-	<b>TOSC</b>		
	Back-Porch	<b>THB</b>		38		<b>TOSC</b>		
	Front-Porch	<b>THF</b>		20		<b>TOSC</b>		
	Hsync rising time	<b>TCr</b>	-	-	700	ns		
	Hsync falling time	<b>TCf</b>	-	-	300	ns		
Vsync	Vsync period	NTSC	-	262.5	-	<b>TH</b>		
		PAL	-	312.5	-	<b>TH</b>		
	Vsync pulse width	<b>TVS</b>	1	3	5	<b>TH</b>		
	Back-Porch	NTSC	<b>TVB</b>		15		<b>TH</b>	
		PAL			23		<b>TH</b>	
	Display Period	<b>TVD</b>		240		<b>TH</b>		
	Front Porch	NTSC	<b>TVF</b>		4.5		<b>TH</b>	
		PAL			46.5		<b>TH</b>	
	Vsync rising time	<b>TVr</b>	-	-	700	ns		
	Vsync falling time	<b>TVf</b>	-	-	1.5	$\mu$ s		
	Vsync falling to Hsync rising time for odd field	<b>THVO</b>	1	-	-	<b>TOSC</b>		
Vsync falling to Hsync falling time for even field	<b>THVE</b>	1	-	-	<b>TOSC</b>			
ENAB	Vsync-DEN time	NTSC	<b>TVSE</b>	-	18	-	<b>TH</b>	
		PAL	<b>TVSE</b>	-	26	-	<b>TH</b>	
	Hsync-DEN time	<b>THE</b>	36	68	88	<b>TOSC</b>		
	DEN plus width	<b>TEP</b>	-	320	-	<b>TOSC</b>		

Note : If ENAB is fixed to low, the SYNC mode is used. Otherwise DE mode is used. When SYNC mode is used, 1st data start from 68th CK after Hsync falling

- Horizontal display timing

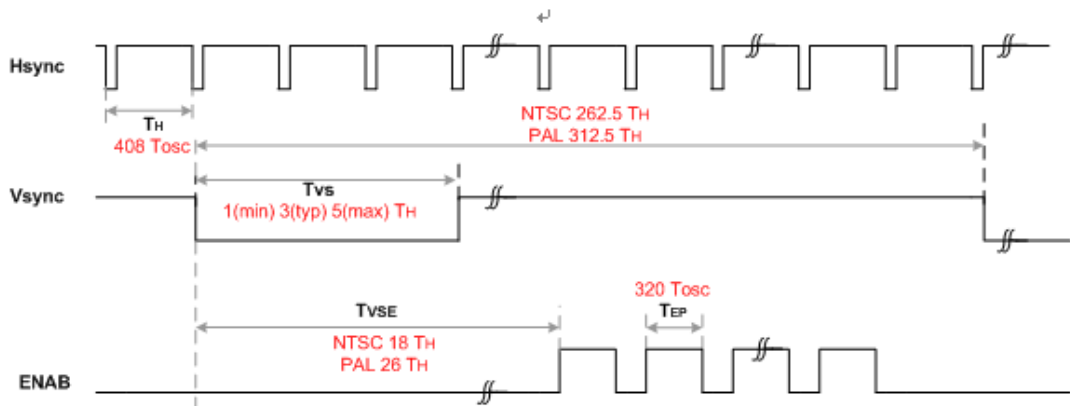
Note: Data is latched rising edge trigger of CLK.



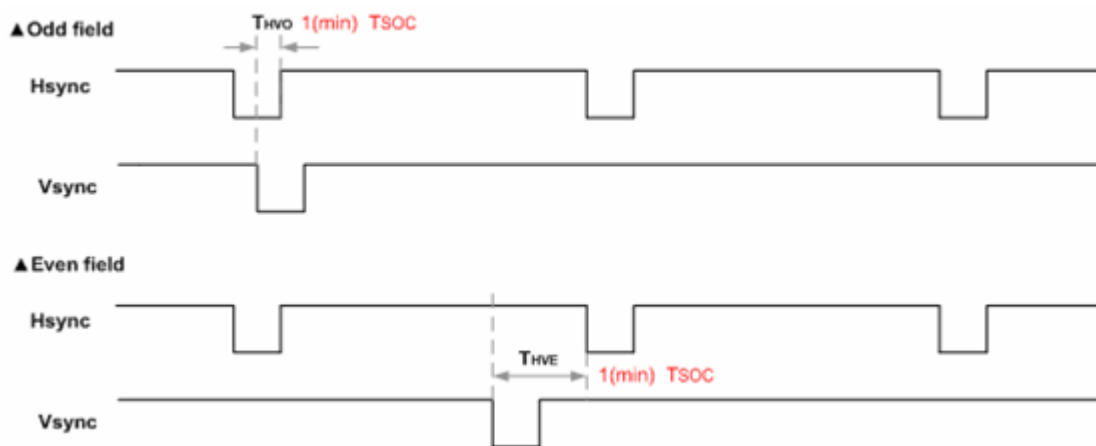
If ENAB is fixed to low, the SYNC mode is used. Otherwise DE mode is used.  
When SYNC mode is used, 1st data start from 68th CK after Hsync falling

- Vertical display timing

- Vertical display timing



- Hsync and Vsync timing





## 4 Optical specification

### 4.1 Optical characteristic of the LCD

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Response Time	Rise	$T_r$	$\Theta=0^\circ$	-	15	30	ms	Note 1,2,3,5
	Fall	$T_f$		-	35	50	ms	
Contrast ratio		CR	At optimized viewing angle		350	-		Note 1,2,4,5
Viewing Angle	Top		$CR \geq 10$	55	60	-	deg.	Note1,2, 5,6
	Bottom			45	50	-		
	Left			55	60	-		
	Right			55	60	-		
Brightness		$Y_L$	$I_{LED}=240mA,$ $25^\circ C$	600	700	-	$cd/m^2$	Note 7
Brightness Uniformity				75	--	--	%	Note 8
Red chromaticity	XR	$\Theta=0^\circ$ $\Theta=0^\circ$		0.582	0.622	0.672		Note 7 For reference only. These data should be update according the prototype.
	YR			0.326	0.366	0.406		
Green chromaticity	XG			0.317	0.357	0.397		
	YG			0.520	0.560	0.600		
Blue chromaticity	XB			0.101	0.141	0.181		
	YB			0.055	0.095	0.135		
White chromaticity	XW			0.277	0.327	0.367		
	YW			0.298	0.348	0.388		

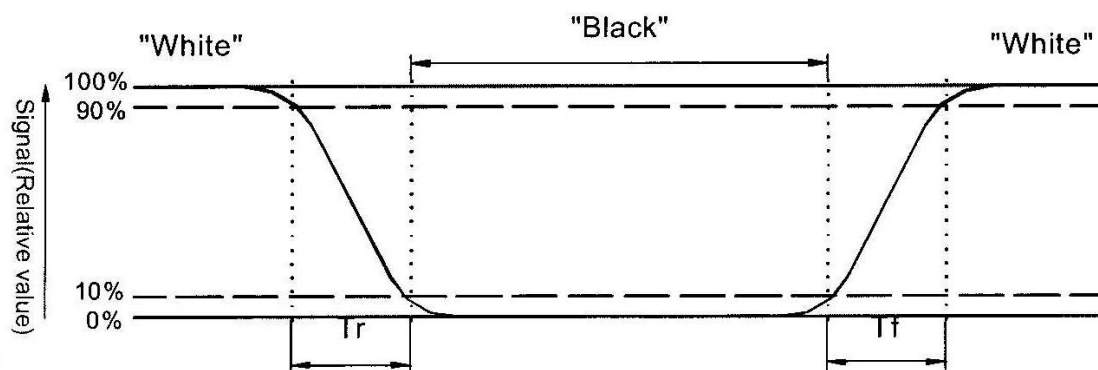
( ) For reference only. These data should be update according the prototype.

Note 1: Ambient temperature= $25^\circ C$ , and  $I_{LED}=240 mA$ . To be measured in the dark room.

Note 2: To be measured on the center area of panel with a viewing cone of  $1^\circ$  by Topcon luminance meter BM-7, after 10 minutes operation.

Note 3. Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



Note 4. Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

$$\text{Contrast ratio(CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector Output when LCD is at "Black" state}}$$

Note 5: White  $V_i = V_{i50} + 1.5V$

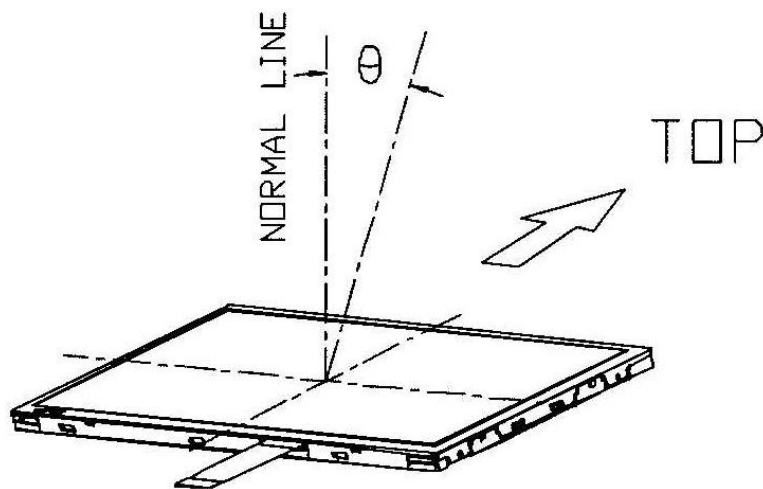
Black  $V_i = V_{i50} + 2.0V$

“±” means that the analog input signal swings in phase with  $V_{COM}$  signal.

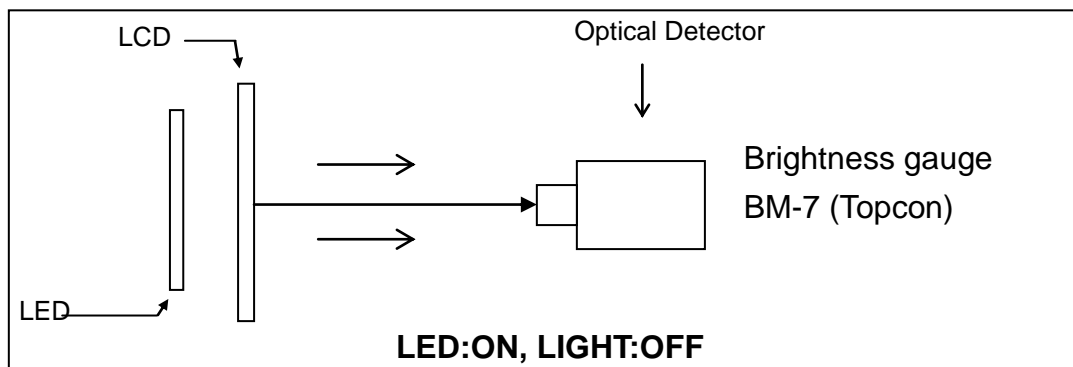
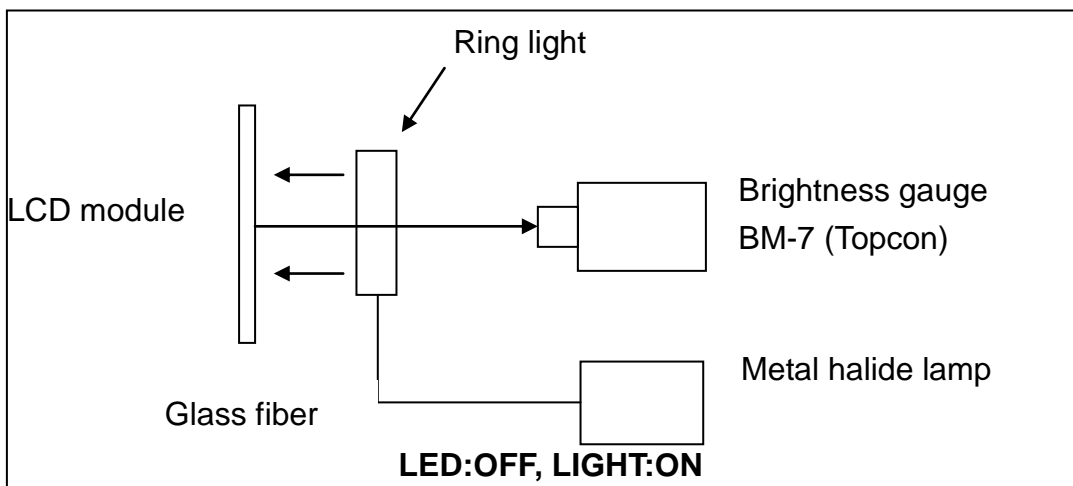
“ $\frac{-}{+}$ ” means that the analog input signal swings out of phase with  $V_{COM}$  signal.

$V_{i50}$  : The analog input voltage when transmission is 50%. The 100% Transmission is defined as the transmission of LCD panel when all the Input terminals of module are electrically opened.

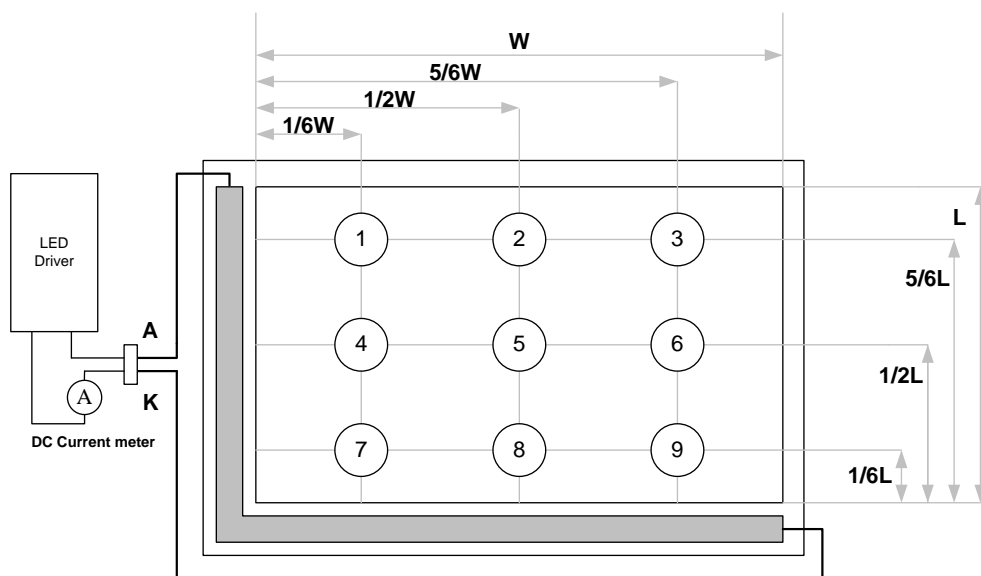
Note 6. Definition of viewing angle, Refer to figure as below.



Note 7. Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.



Note 8: The Uniformity definition  
 $(\text{Min Brightness} / \text{Max Brightness}) \times 100\%$

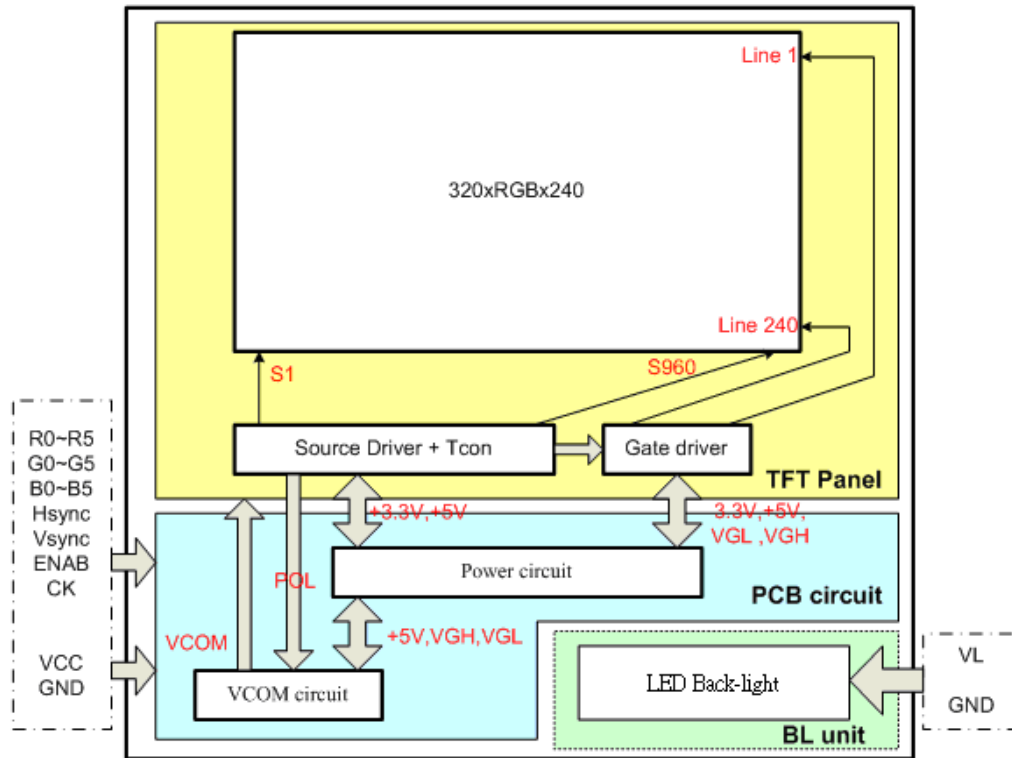


## 5 Interface specifications

### 5.1 Driving signals for the TFT panel

Pin no	Symbol	I/O	Description	Remark
1	GND		Gound	
2	CK		Clock signal. Latching data at the rising edge	
3	Hsync		Horizontal sync input in digital RGB mode	
4	Vsync		Vertical sync input in digital RGB mode	
5	GND	I	Gound	
6	R0	I	Red data	
7	R1	I		
8	R2	I		
9	R3	I		
10	R4	I		
11	R5	I		
12	GND	I	Gound	
13	G0	I	Green data	
14	G1	I		
15	G2	I		
16	G3	I		
17	G4	I		
18	G5	I		
19	GND	I	Gound	
20	B0	I	Blue data	
21	B1	I		
22	B2	I		
23	B3			
24	B4	I		
25	B5	I		
26	GND	I	Gound	
27	ENAB	I	Input data enable control	
28	VCC	I	+3.3V Power Supply	
29	VCC	I		
30	R/L	I	Scan direction R/L=H Left to Right R/L=L Right to Left	Note 1
31	U/D	I	Scan direction U/D=H Top to Bottom U/D=L Bottom to Top	Note 1
32	NC	I	Not use	
33	GND	I	Gound	

## 6 BLOCK DIAGRAM



## 7 DISPLAYED COLOR AND INPUT DATA

	Color & Gray Scale	DATA SIGNAL																	
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0)	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
	Blue(0)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(61)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(31)	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(1)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Green	Red(0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(61)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(31)	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Blue	Green(1)	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0	0
	Green(0)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(31)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Blue	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0
	Blue(0)	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1

## 8 QUALITY AND RELIABILITY

### 8.1 TEST CONDITIONS

Tests should be conducted under the following conditions :

Ambient temperature :  $25 \pm 5^{\circ}\text{C}$

Humidity :  $60 \pm 25\% \text{ RH}$ .

### 8.2 SAMPLING PLAN

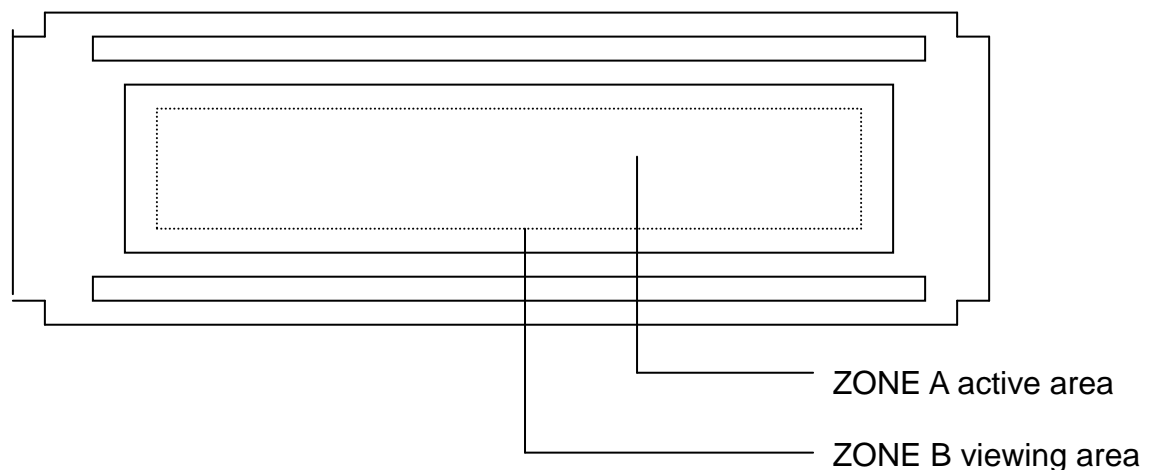
Sampling method shall be in accordance with MIL-STD-105E , level II, normal single sampling plan .

### 8.3 ACCEPTABLE QUALITY LEVEL

A major defect is defined as one that could cause failure to or materially reduce the usability of the unit for its intended purpose. A minor defect is one that does not materially reduce the usability of the unit for its intended purpose or is an infringement from established standards and has no significant bearing on its effective use or operation.

### 8.4 APPEARANCE

An appearance test should be conducted by human sight at approximately 30 cm distance from the LCD module under fluorescent light. The inspection area of LCD panel shall be within the range of following limits.



## 8.5 RELIABILITY

Test Item	Test Conditions	Note
High Temperature Operation	70±3°C , t=240 hrs	
Low Temperature Operation	-20±3°C , t=240 hrs	
High Temperature Storage	80±3°C , t=240 hrs	1,2
Low Temperature Storage	-30±3°C , t=240 hrs	1,2
Thermal Shock Test	-20°C ~ 25°C ~ 70°C 30 min. 5 min. 30 min. ( 1 cycle ) Total 5 cycle	1,2
Humidity Test	40 °C, Humidity 90%, 96 hrs	1,2
Vibration Test (Packing)	Sweep frequency : 10 ~ 55 ~ 10 Hz/1min Amplitude : 0.75mm Test direction : X.Y.Z/3 axis Duration : 30min/each axis	2

Note 1 : Condensation of water is not permitted on the module.

Note 2 : The module should be inspected after 1 hour storage in normal conditions (15-35°C , 45-65%RH).

Definitions of life end point :

- Current drain should be smaller than the specific value.
- Function of the module should be maintained.
- Appearance and display quality should not have degraded noticeably.
- Contrast ratio should be greater than 50% of the initial value.



## **USE PRECAUTIONS**

### **9 HANDLING PRECAUTIONS**

- (1) An LCD module is a fragile item and should not be subjected to strong mechanical shocks.
- (2) Avoid applying pressure to the module surface. This will distort the glass and cause a change in colour.
- (3) Under no circumstances should the position of the bezel tabs or their shape be modified.
- (4) Do not modify the display PCB in either shape or positioning of components.
- (5) Do not modify or move location of the zebra or heat seal connectors.
- (6) The device should only be soldered to during interfacing. Modification to other areas of the board should not be carried out.
- (7) In the event of LCD breakage and resultant leakage of fluid do not inhale, ingest or make contact with the skin. If contact is made rinse immediately.
- (8) When cleaning the module use a soft damp cloth with a mild solvent, such as Isopropyl or Ethyl alcohol. The use of water, ketone or aromatic is not permitted.
- (9) Prior to initial power up input signals should not be applied.
- (10) Protect the module against static electricity and observe appropriate anti-static precautions.

#### **9.1 Installing precautions**

- 1) The PCB has many ICs that may be damaged easily by static electricity. To prevent breaking by static electricity from the human body and clothing, earth the human body properly using the high resistance and discharge static electricity during the operation. In this case, however, the resistance value should be approx.  $1M\Omega$  and the resistance should be placed near the human body rather than the ground surface. When the indoor space is dry, static electricity may occur easily so be careful. We recommend the indoor space should be kept with humidity of 60% or more. When a soldering iron or other similar tool is used for assembly, be sure to earth it.
- 2) When installing the module and ICs, do not bend or twist them. Failure to do so may crack LC element and cause circuit failure.
- 3) To protect LC element, especially polarizing plate, use a transparent protective plate (e.g., acrylic plate, glass etc) for the product case.

- 4) Do not use an adhesive like a both-side adhesive tape to make LCD surface (polarizing plate) and product case stick together. Failure to do so may cause the polarizing plate to peel off.

## **9.2 Storage precautions**

- 1) Avoid a high temperature and humidity area. Keep the temperature between 0°C and 35°C and also the humidity under 60%.
- 2) Choose the dark spaces where the product is not exposed to direct sunlight or fluorescent light.
- 3) Store the products as they are put in the boxes provided from us or in the same conditions as we recommend.

## **9.3 Operating precautions**

- 1) Do not boost the applied drive voltage abnormally. Failure to do so may break ICs. When applying power voltage, check the electrical features beforehand and be careful. Always turn off the power to the LC module controller before removing or inserting the LC module input connector. If the input connector is removed or inserted while the power is turned on, the LC module internal circuit may break.
- 2) The display response may be late if the operating temperature is under the normal standard, and the display may be out of order if it is above the normal standard. But this is not a failure; this will be restored if it is within the normal standard.
- 3) The LCD contrast varies depending on the visual angle, ambient temperature, power voltage etc. Obtain the optimum contrast by adjusting the LC drive voltage.
- 4) When carrying out the test, do not take the module out of the low-temperature space suddenly. Failure to do so will cause the module condensing, leading to malfunctions.
- 5) Make certain that each signal noise level is within the standard (L level: 0.2V<sub>dd</sub> or less and H level: 0.8V<sub>dd</sub> or more) even if the module has functioned properly. If it is beyond the standard, the module may often malfunction. In addition, always connect the module when making noise level measurements.
- 6) The CMOS ICs are incorporated in the module and the pull-up and pull-down function is not adopted for the input so avoid putting the input signal open while the power is ON.
- 7) The characteristic of the semiconductor element changes when it is exposed

to light emissions, therefore ICs on the LCD may malfunction if they receive light emissions. To prevent these malfunctions, design and assemble ICs so that they are shielded from light emissions.

- 8) Crosstalk occurs because of characteristics of the LCD. In general, crosstalk occurs when the regularized display is maintained. Also, crosstalk is affected by the LC drive voltage. Design the contents of the display, considering crosstalk.

#### **9.4 Other**

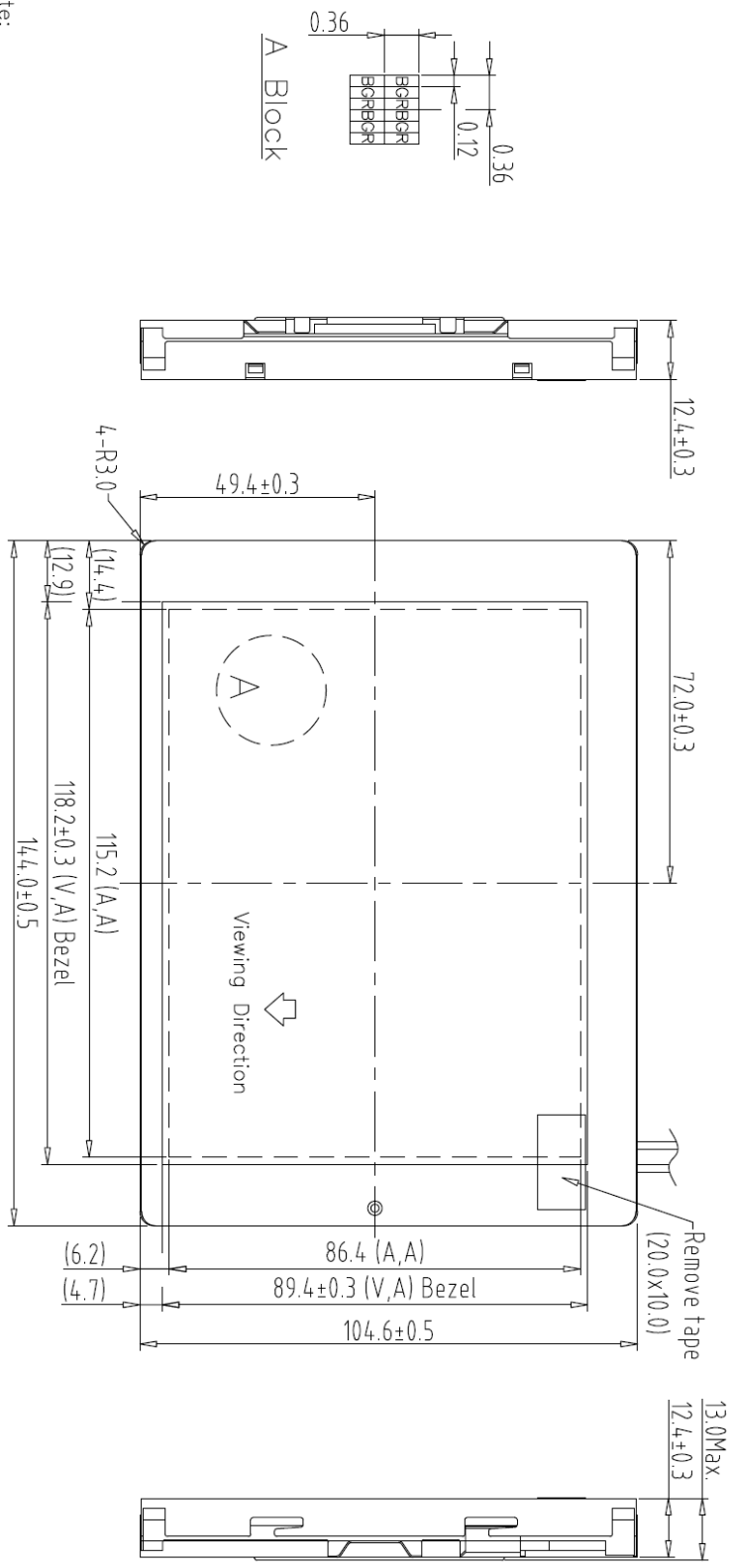
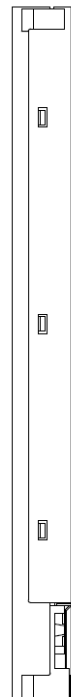
- 1) Do not disassemble or take the LC module into pieces. The LC modules once disassembled or taken into pieces are not the guarantee articles.
- 2) The residual image may exist if the same display pattern is shown for hours. This residual image, however, disappears when another display pattern is shown or the drive is interrupted and left for a while. But this is not a problem on reliability.
- 3) AMIPRE will provide one year warranty for all products and three months warrantee for all repairing products.

# 10 OUTLINE DIMENSION

**PRELIMINARY**

The contents of this document are confidential and must not be disclosed wholly or in part to any third party without the prior written consent of Alpine Co., Ltd.

REV.	REVISION RECORD	DATE	NAME
0	NEW RELEASE	12-16-15	SNOW



Note:

1. Unless indicated, Tolerance "±0.3"
2. UV Glue For OLB Protection.
3. Main LCD 320x240 (R.G.B) TFT LCD => 5.7" TFT LCD

1	320240NSQ-07	7		TOLERANCE GRADE(F)	A	B	DIM.	MM	DWN.	SNOW	DATE	TITLE
2		8					IE NO.		CHK.		DATE	
3		9					PARTS NO.	LCM	APPD.		DATE	DWG. NO.
4		10										*151266MA
5		11										SHEET
6		12										1 OF 1

